

**Bachelor of Science in Engineering Technology (BSET):
Major in Electrical Engineering Technology (ELET)**
Sample Four-Year Schedule (124 credits effective FALL 2013)

FRESHMAN YEAR

FALL Course / [prerequisites] / [normal offering]	CR	SPRING Course / [prerequisites] / [normal offering]	CR
MATH 1710, Cal I [MATH 1650 or MATH 1600,1610] [FA, SP]	4	MATH 1720, Cal II [MATH 1710] [FA, SP]	3
ENGR 1030, Tech Systems [None] [FA]	3	PHYS 1710/1730, Mech & Lab [MATH 1710] [FA, SP, SU]	4
ELET 1720 Intro to Electronics [MATH 1100] [FA]	3	ELET 2740, Special Electronic Dev [ELET 1720 & credit for or concurrent enrollment with MATH1710] [SP]	4
ENGL 1310, College Writing OR TECM 1700 [None] [FA, SP]	3	ENGR 2720, Digital Logic [Engineering or engineering technology majors] [FA, SP]	3
HSSE [Choose from list] [FA, SP]	3	ENGR 2730, Digital Logic Lab [Concurrent with ENGR 2720] [FA, SP]	1
Total Hours	16	Total Hours	15

SOPHOMORE YEAR

FALL Course / [prerequisites] / [normal offering]	CR	SPRING Course / [prerequisites] / [normal offering]	CR
PHYS 2220/2240, Electricity & Magnetism & Lab [MATH 1720, PHYS 1710] [FA, SP, SU]	4	CHEM 1410/1430 [MATH 1100] Gen Chem for Sc OR CHEM 1415/1435 [MATH 1650], Genl Chem for Eng [FA, SP, SU]	4
ENGR 2405/2415, Circuits Analysis & Lab [MATH 1720; Concurrent with PHYS 2220/2240] [FA, SP]	4	ELET 3980 Dig Ctrl of Ind Proc [MATH 1650] [SP]	3
ENGR 2750, Intro to Microprocessors [ENGR 2720] [FA]	4	ENGR 1060, Comm & Ethics [ENGL 1310] [SP]	3
TECM 2700, Technical Writing [ENGL 1310] [FA, SP]	3	HSSE [Choose from list] [FA, SP]	3
HSSE [Choose from list] [FA, SP]	3	HSSE [Choose from list] [FA, SP]	3
<i>Expect to have completed MATH, PHYS, CHEM; ELET 1720, 2740, 3980; ENGR1030, 1060, 2405/2415, 2720/2730, 2750</i>			X
Total Hours	15-18	Total Hours	16

JUNIOR YEAR

FALL Course / [prerequisites] / [normal offering]	CR	SPRING Course / [prerequisites] / [normal offering]	CR
ELET 3700, Circuit Analysis [ENGR 2405] [FA, SP]	4	ELET 3760, Design of DSP Systems [ELET 3700] [FA, SP]	4
ELET 3750, Embedded C Prog. [ENGR 2750] [FA, SP]	4	ELET 4340, Digital Logic Design Tech [ELET 3750] [FA, SP]	3
Dir Tech ELECT [Choose from list] [FA, SP]	3	ELET 3220 Intro to Power Sys Analysis [ELET 2405] [SP]	3
Dir Tech ELECT [Choose from list] [FA, SP]	3	Dir Tech ELECT [Choose from list] [FA, SP]	3
HSSE [If needed; Choose from list] [FA, SP]	3	HSSE [If needed; Choose from list] [FA, SP]	3
Total Hours	14-17	Total Hours	13-16

SENIOR YEAR

FALL Course / [prerequisites] / [normal offering]	CR	SPRING Course / [prerequisites] / [normal offering]	CR
ELET 4710, Electronic Comm I [ELET 3700] [FA]	4	ELET 4320, Electronic Comm II [ELET 4710] [SP]	3
ELET 4720, Control Systems [ELET 3700] [FA]	4	ELET 4330 Instrumentation Sys Design [ELET 3760] [SP]	3
ELET 4780, Senior Design [ELET 3760, SR Standing] [FA]	1	ELET 4790, Senior Design II [ELET 4780] [SP]	3
ELET 4300, Embedded Sys Org [ELET 4340] [FA]	3	Dir Tech ELECT [Choose from list] [FA, SP]	2
Dir Tech ELECT [Choose from list] [FA, SP]	3	HSSE [If needed; Choose from list] [FA, SP]	3
Total Hours	15	Total Hours	11-14

PLEASE NOTE:

This is an unofficial sample schedule. Students should meet with their advisor each semester for individual scheduling and program decisions. Engineering admissions requirements must be met & a degree audit must be created in order to transition from pre-engineering to full major and progress in the program.

